



SM2258XT Hynix 3D V4 Flash F/W & ISP Release Information – R0330A0

Introduction

This purpose of this document is to provide release information on the SM22X family F/W and ISP release information

Fix Coverage

- stands for the “new fix” or “new support” in the category
- stands for the “no-update” in the category

<input checked="" type="checkbox"/> MP Tool	<input checked="" type="checkbox"/> Controller ISP
<input type="checkbox"/> Yield Issue <input type="checkbox"/> Flash Issue <ul style="list-style-type: none"> <input checked="" type="checkbox"/> 3D TLC Flash <ul style="list-style-type: none"> <input type="checkbox"/> Samsung Flash <input type="checkbox"/> Toshiba/Sandisk Flash <input type="checkbox"/> Intel/Micron Flash <input type="checkbox"/> Hynix Flash <input type="checkbox"/> Others <input type="checkbox"/> MP Tool Bug Fix <input checked="" type="checkbox"/> MP Tool New Function <input checked="" type="checkbox"/> Feature Enhance	<input type="checkbox"/> Yield Issue <input type="checkbox"/> Flash Issue <ul style="list-style-type: none"> <input checked="" type="checkbox"/> 3D TLC Flash <ul style="list-style-type: none"> <input type="checkbox"/> Samsung Flash <input type="checkbox"/> Toshiba/Sandisk Flash <input type="checkbox"/> Intel/Micron Flash <input checked="" type="checkbox"/> Hynix Flash <input type="checkbox"/> Others <input checked="" type="checkbox"/> ISP Bug Fix <input checked="" type="checkbox"/> Feature Enhance



ISP Revision History

Package	ISP Version	MP Tool version	RDT version	Note
R0330A	R0330A0	R0323A	R0206A0	ISP: <ol style="list-style-type: none"> 1. Switch to dynamic fix boundary. 2. Adjust dynamic fix boundary strategy. 3. Fix UGSD active full over program issue. 4. Modify power on GC threshold 5. Fix queue full bug 6. Hynix v4 enable cache read 7. Fix 2channel GC partial load f2h 18k bug 8. Fix read count bug 9. After background Gc disable hd tune sustain write function MPTool: <ol style="list-style-type: none"> 1. Modify new MPISP Tag. 2. Modify that select "Check & Keep RDT" option and do pretest then also need to pre-check capacity enough or not? 3. Add new errorcode 0x09/0x0A for _SMIPtestGenDiffAddrTable(). 4. Always disable Internal/External interleave way setting.
R0205A	R0124A0	R0205A	Q1213A0	ISP <ol style="list-style-type: none"> 1. First formal release to support Hynix 3D V4 256Gb per die.

Note:

1. F/W and ISP update is recommended.
2. History # is denoted by "Version-Date" .

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