



SM2258XT 2D Flash F/W & ISP Release Information – Q0213A

Introduction

This purpose of this document is to provide release information on the SM22X family F/W and ISP release information

Fix Coverage

- stands for the “new fix” or “new support” in the category
- stands for the “no-update” in the category

| <input checked="" type="checkbox"/> MP Tool | <input checked="" type="checkbox"/> Controller ISP |
|--|---|
| <input type="checkbox"/> Yield Issue <input type="checkbox"/> Flash Issue <ul style="list-style-type: none"> <input type="checkbox"/> SLC Flash <ul style="list-style-type: none"> <input type="checkbox"/> Samsung Flash <input type="checkbox"/> Toshiba/Sandisk Flash <input type="checkbox"/> Intel/Micron Flash <input type="checkbox"/> Hynix Flash <input type="checkbox"/> Others <input type="checkbox"/> MLC Flash <ul style="list-style-type: none"> <input type="checkbox"/> Samsung Flash <input type="checkbox"/> Toshiba/Sandisk Flash <input type="checkbox"/> Intel/Micron Flash <input type="checkbox"/> Hynix Flash <input type="checkbox"/> Others <input type="checkbox"/> MP Tool Bug Fix <input checked="" type="checkbox"/> MP Tool New Function <input checked="" type="checkbox"/> Feature Enhance | <input type="checkbox"/> Yield Issue <input checked="" type="checkbox"/> Flash Issue <ul style="list-style-type: none"> <input type="checkbox"/> TLC Flash <ul style="list-style-type: none"> <input type="checkbox"/> Samsung Flash <input checked="" type="checkbox"/> Toshiba/Sandisk Flash <input type="checkbox"/> Intel/Micron Flash <input checked="" type="checkbox"/> Hynix Flash <input type="checkbox"/> Others <input checked="" type="checkbox"/> ISP Bug Fix <input checked="" type="checkbox"/> Feature Enhance |



ISP Revision History

| Version | MP Tool version | RDT version | Note |
|---------|-----------------|-------------|---|
| Q0213A | Q0213B | Q0103A0 | <p>ISP:</p> <ol style="list-style-type: none"> 1 First formal release to support HY14/HY16/SNDK in one package for both DDR200 and DDR400. 2 Enable _EN_NAND_SEQ_CACHE_MODE and _EN_POSTWRITEREAD for H14/H16/Sandisk 1Z. 3 Modify initial value of gsGclInfo.u32GcLeastBudget for H14. 4 Modified the SMART attribute 05,C3,C5 for mp tool option 5 Add ReadbRetryTable to 35's set(H16). 6 Add DDR400 for Sandisk1z. 7 Modify FlashClock 200 to 193(DDR387.5). 8 Set Schmitt windows as 0x20 for HY16. 9 Reduce gsGclInfo.u32GcCachebTime for Sandisk 1z/ HY14 for HD tune performance. <p>MPISP:</p> <ol style="list-style-type: none"> 1. Modify Flash Clock 200 to 193(DDR387.5) <p>RDT</p> <ol style="list-style-type: none"> 1. Fixed SLC block < 150 RDT abnormal issue. <p>MPTool:</p> <ol style="list-style-type: none"> 1. Add IDEMA 640GB and remove 360GB/720GB support. 2. Add "SMART 0xC3 be 0" option. ([OPTION] : ENABLESMARTC3BE0) 3. Add "SMART 0x05/0xC5 be 0" option. ([OPTION] : ENABLESMART05C5BE0) <p>Maximum capacity support is 640GB.</p> <ol style="list-style-type: none"> 4. Add some comments for "Test Option" group at [RDT Test Setting] dialog. 5 Fix bug: Disable "Support DevSlp" need to disable 0x9F.b0 not 0x9F.b1 of IDtable. |

Note:

1. F/W and ISP update is recommended.
2. History # is denoted by "Version-Date" .

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