

Revision History

Revision	Date	Description	Page updated	Drawn	Checked	Approval
01	2018.01.22	Initial release	--	Garyweng	Barry Chang	
02	2018.03.19	Add C35 220uF/6.3V Cap	2	Garyweng	Barry Chang	
03	2018.06.27	Modify SM3350 Pin 67,68,108 change to GND(E_FUSE Pin)	2	Garyweng	Barry Chang	
04	2018.10.03	1. Add Cap C36 220uF/6.3V for VCCQ2 and ADD Cap C37 220uF/6.3V for VCC 2. Add D8,D9,D10,D15 X4 ZENER_BZT52C2V0_2.0V(1.91~2.01V) X4 V for VCCQ2(UFS BGA) 3. Add D11,D12,D13,D14 ZENER_BZT52C3V6_3.6V X4 V for SDVCC(UFS BGA) 4. Modify C23,C25 330R change to 100R (improve Discharge resistor VCCQ2 and VCC) 5. Modify R31,R32 330R change to 240R (UFS VCC and UFS VCCQ2 Current limit Max 1.2A to 1.95A) 6. Modify U3,U41 ID8223(Max 1.2A) Change to TI TLV62569DBV (MAX 2.0A)	2	Garyweng	Barry Chang	

